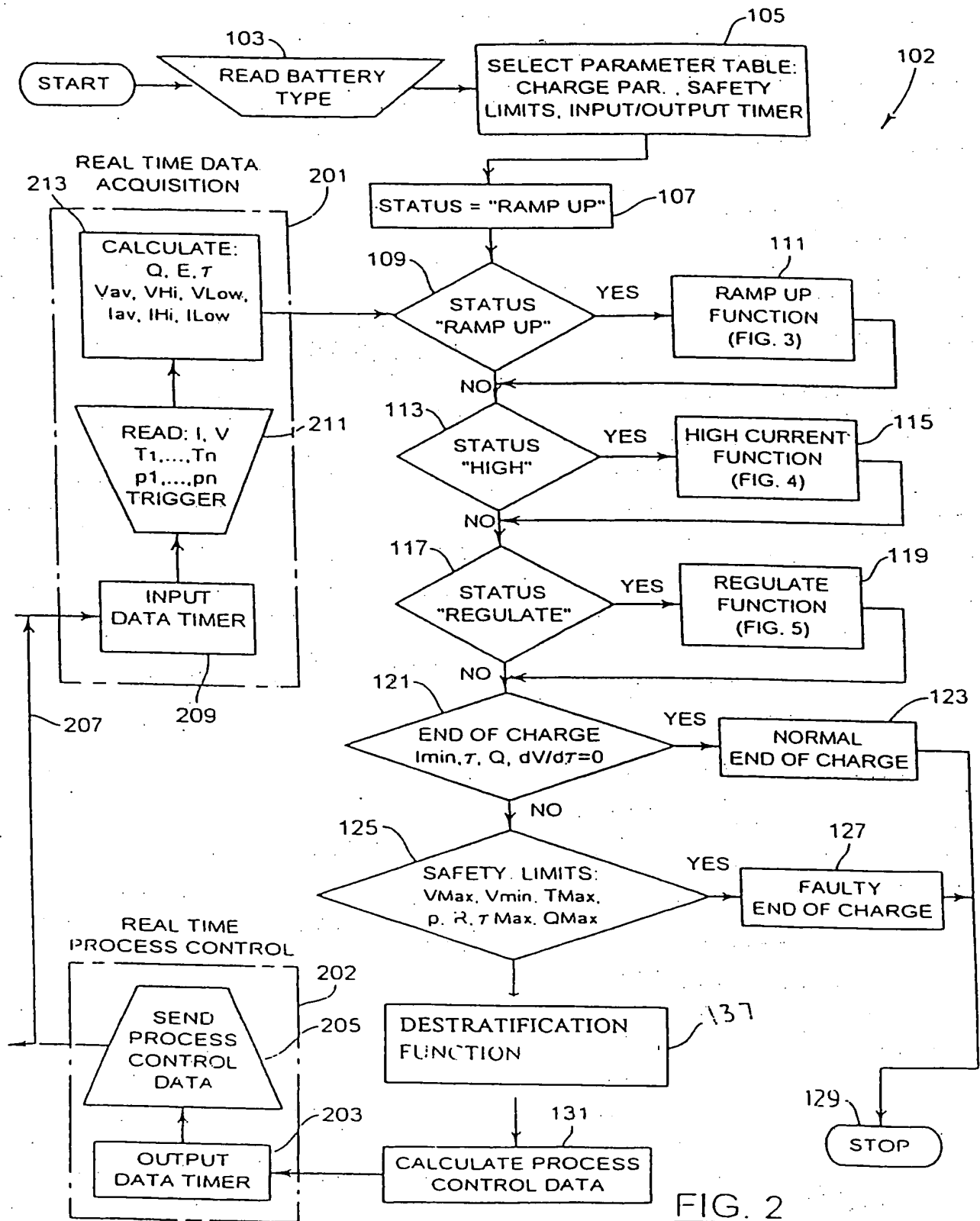


SYSTEM ARCHITECTURE

FIG. 1



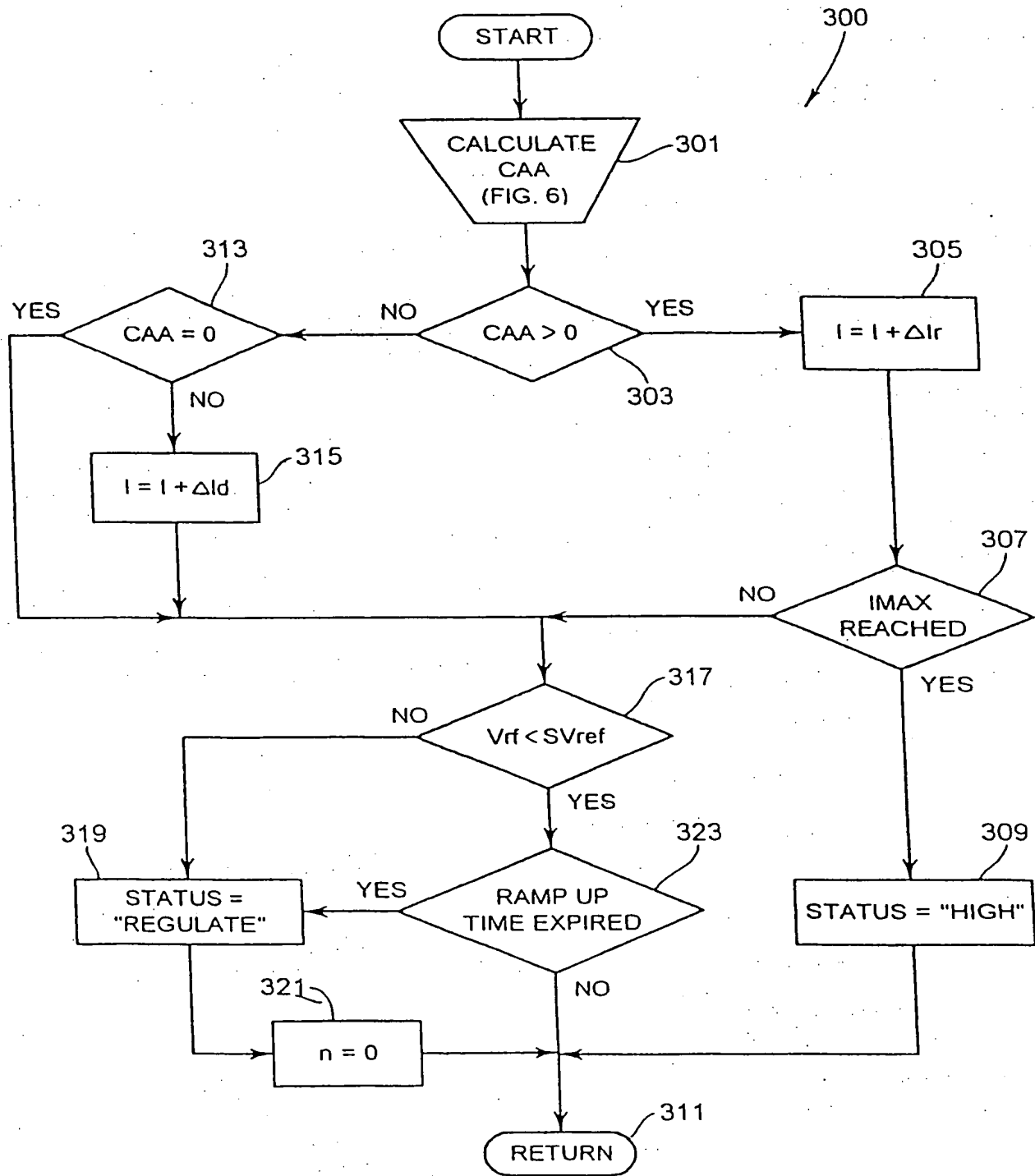


FIG. 3

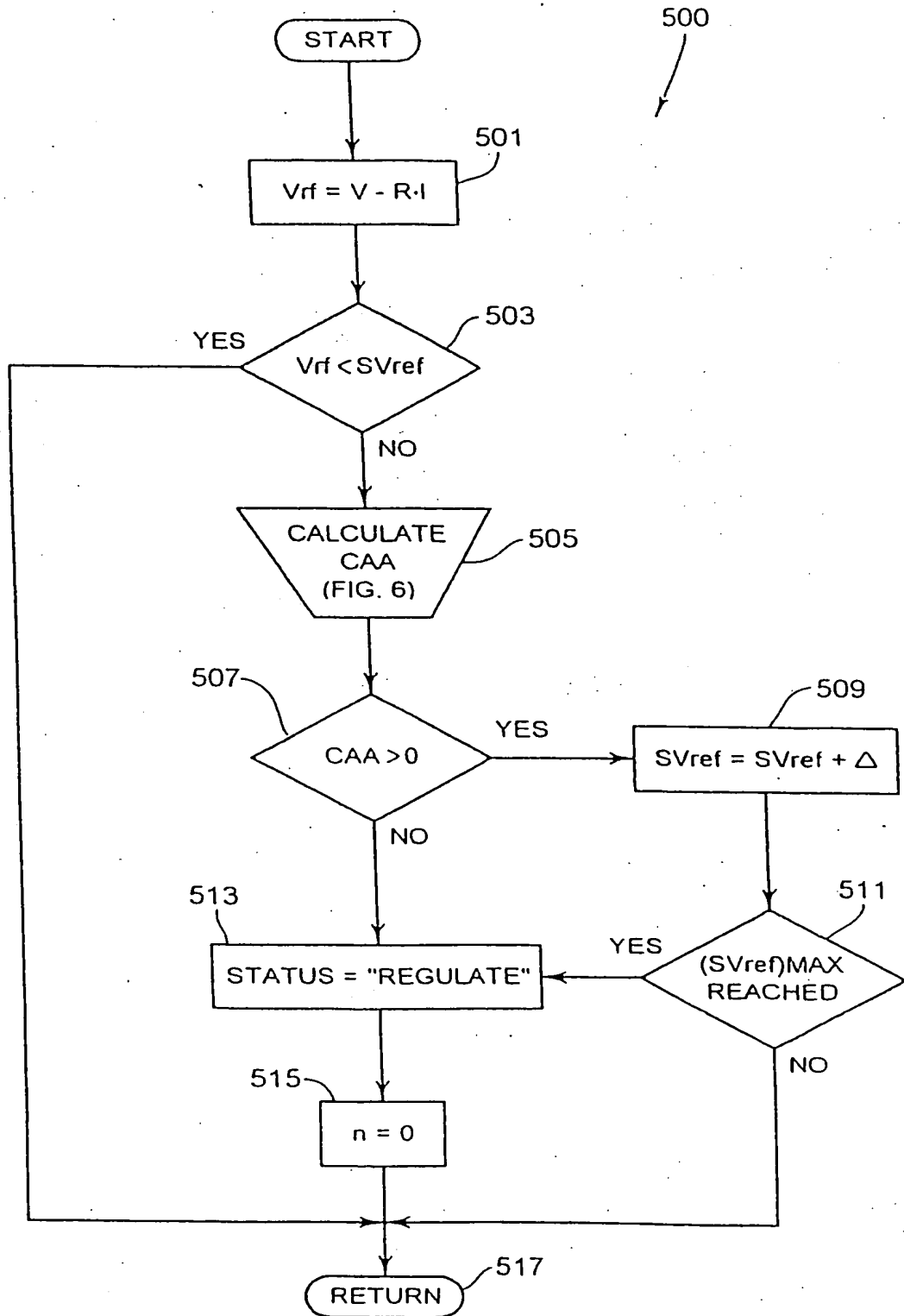
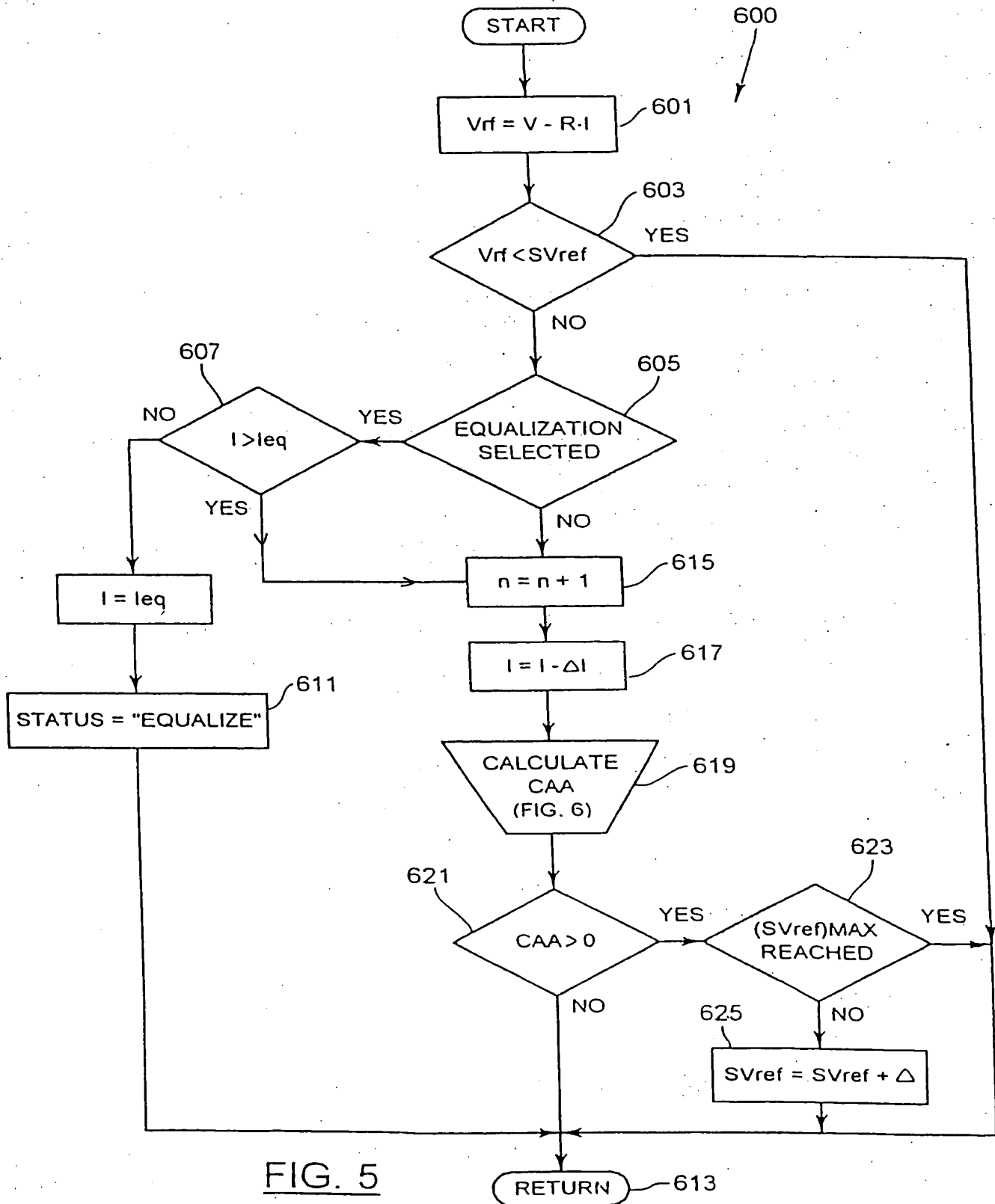


FIG. 4



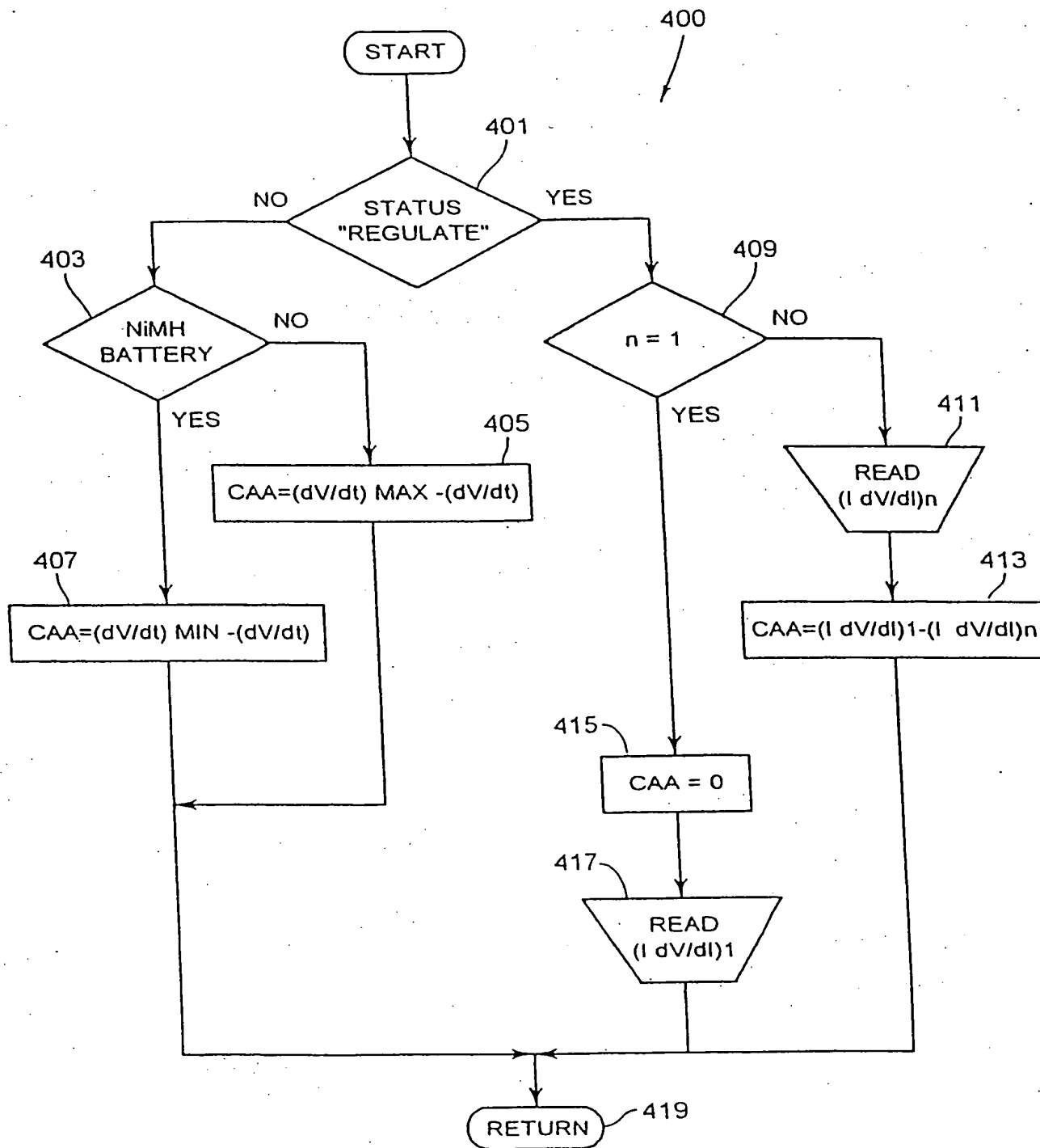


FIG. 6

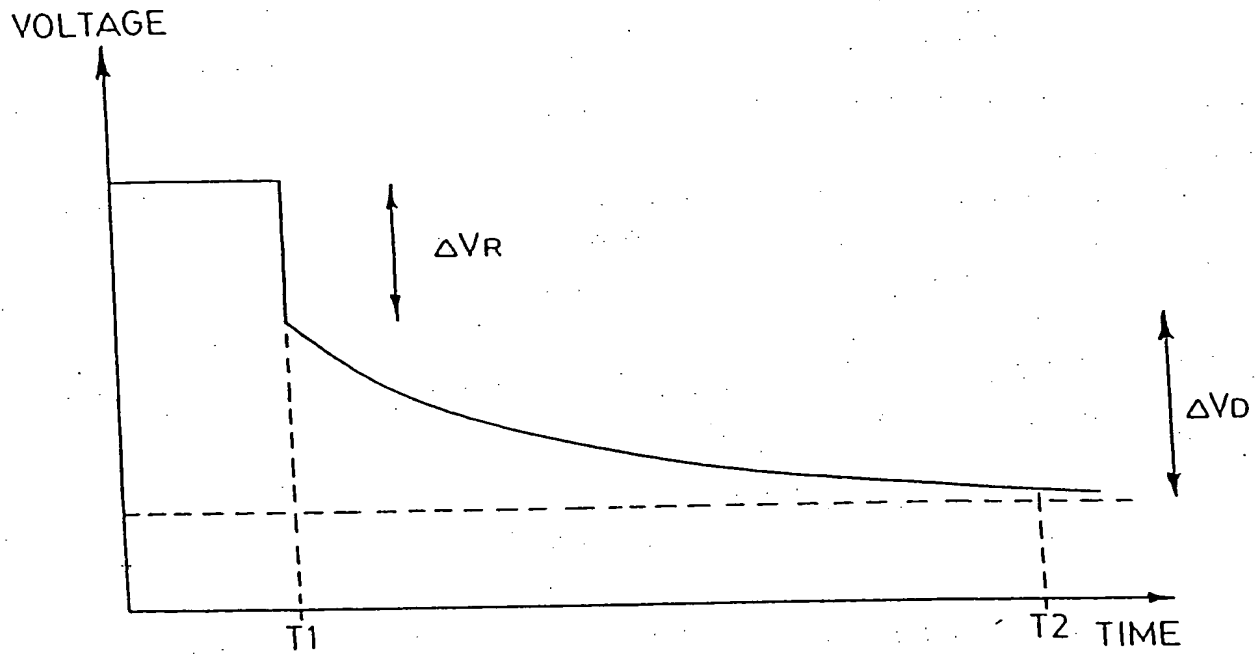


FIG. 7

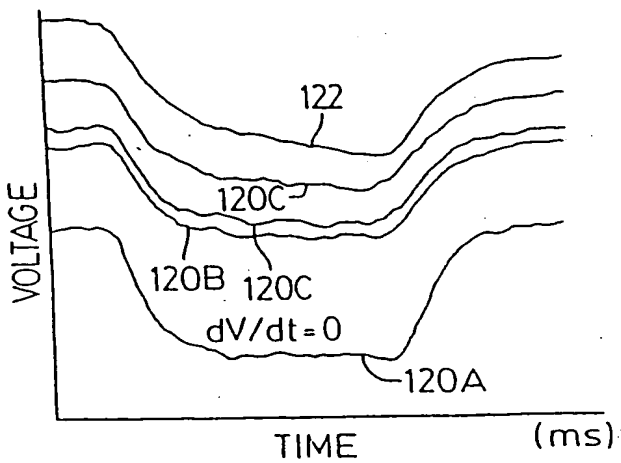


FIG. 8

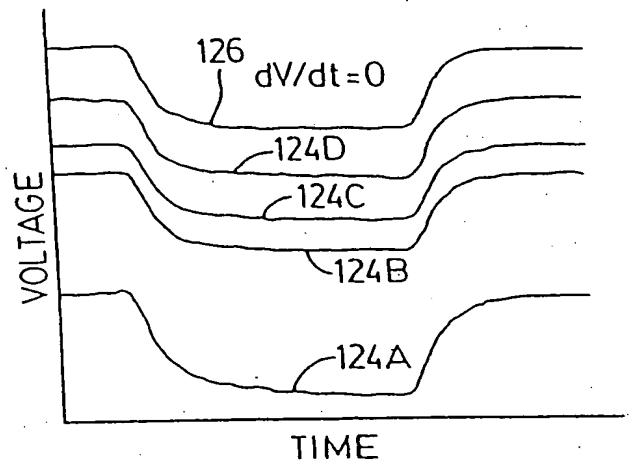
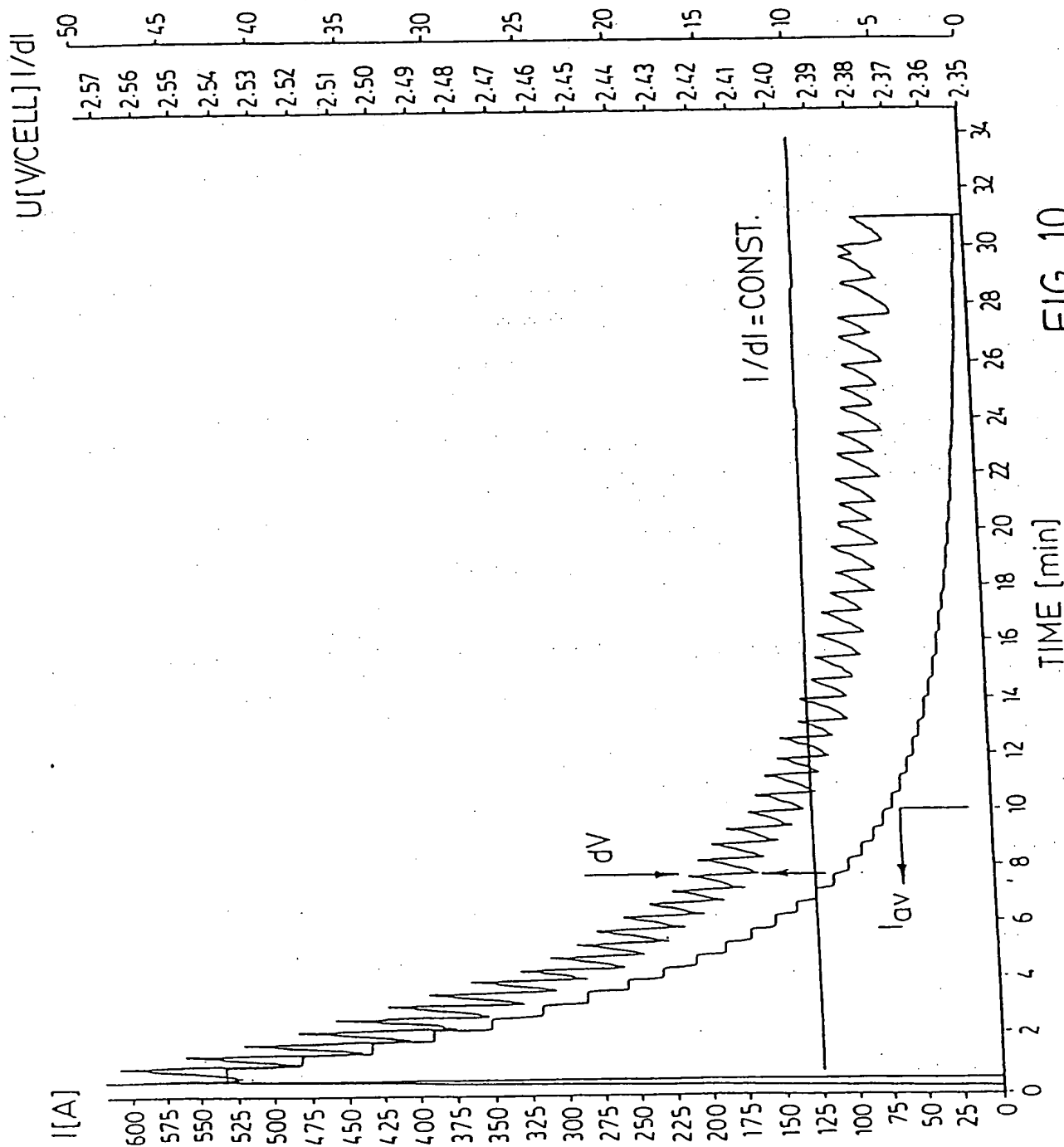


FIG. 9







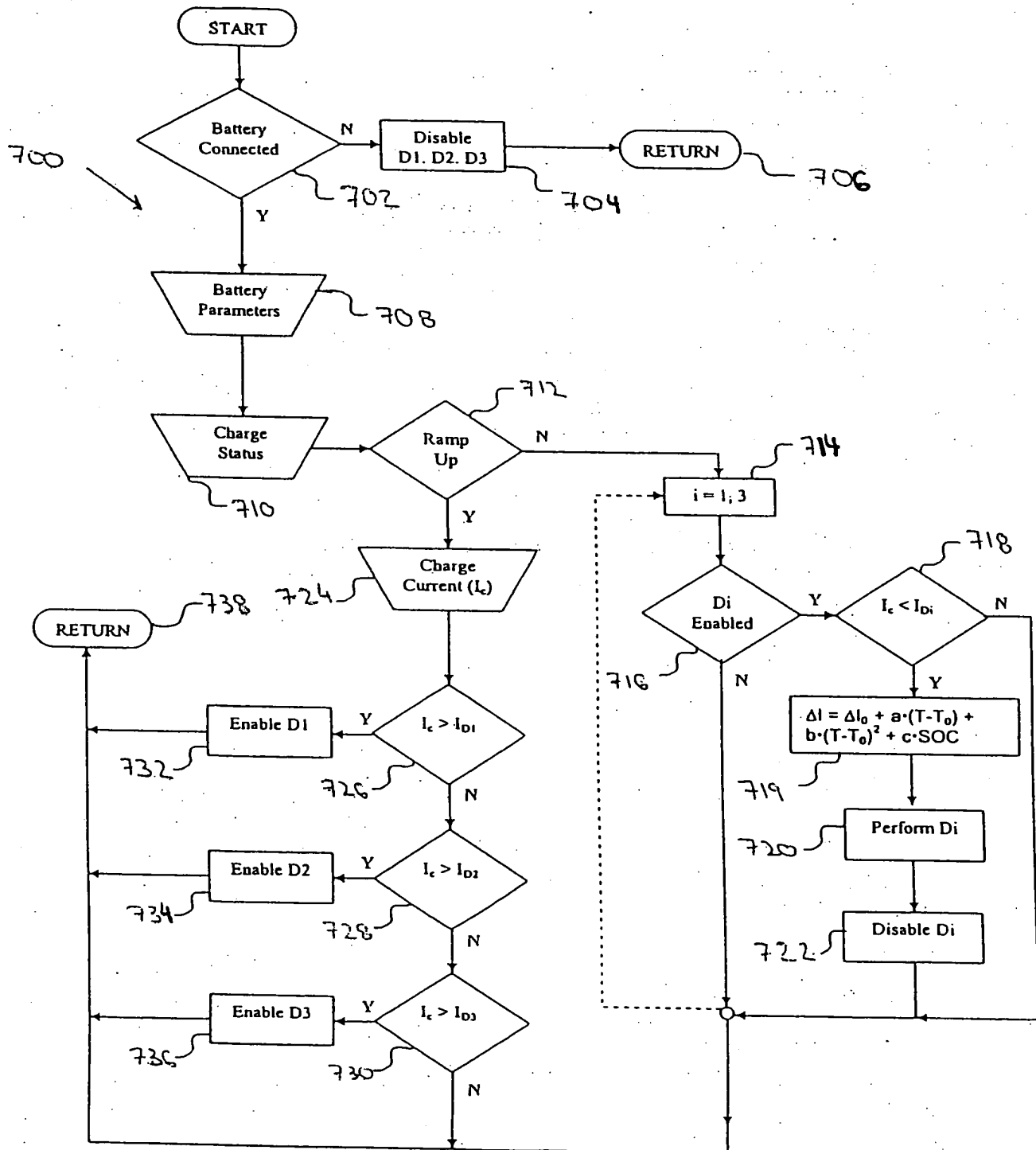
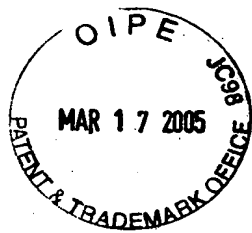


Figure 12